File No.: 9-14774-1US-1-1

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

DAVID F. SKOLL et al.

Serial No.:

Not yet assigned.

Division of Serial No. 09/927,551

Filed:

Concurrently herewith.

Title:

DESIGN ANALYSIS WORKSTATION

FOR ANALYZING INTEGRATED

**CIRCUITS** 

**Agent of Record:** 

Max R. Wood

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Assistant Commissioner for Patents Washington, D.C. 2023l U.S.A.

Sir:

## INFORMATION DISCLOSURE STATEMENT PRIOR TO FIRST OFFICE ACTION

Pursuant to the duty of disclosure under 37 CFR 1.56, copies of the references listed on the attached PTO Form 1449 are submitted herewith.

The Examiner is kindly requested to consider these references during the examination of the above-identified application, making the references of record, and to return an initialed copy of the PTO-1449 Form to the below-signed agent.

In accordance with 37 CFR 1.97(h), the submission of the present information is not to be construed as an admission that such information is, or is considered to be material to patentability.

Respectfully submitted, DAVID F. SKOLL et al.

 $\mathbf{R}_{\mathbf{V}}$ 

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Agent of Record, Registration No. 40,388

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Encls.

Form PTO-1449	U.S. Department of Commerce	ATTY.DOCKET NO.	SERIAL NO.	
(Rev.7-80)	Patent & Trademark Office	9-14774-1US-1-1	Not yet assigned.	
LIST OF REFERENCES CITED BY APPLICANT		APPLICANT		
(Use several sheets if necessary)		DAVID F. SKOLL et al.		
Ì		FILING DATE:	GROUP	
		Concurrently herewith.		
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## U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	. Subclass	Filing Date
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	AB	5,086,477	4 Feb/92	Yu et al.	382	8	7 Aug/90
	AC	5,191,213	2 Mar/93	Ahmed et al.	250	310	5 July/91
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	AF						
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AR	to GDS-11 layout, George Lanzarotta, Raith USA, Inc., November 1999
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AS	Article: "Integrated Circuit Chip Layer Analysis", Tan Ooi Kiang et al., Sept. 1993, Proceedings of the 5 <sup>th</sup> International Symposium on IC Technology, System and Applications, Nanyang Technological University, Singapore.

Examiner	Date considered			
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in				
conformance and not considered. Include copy of this form with next communication to applicant.				